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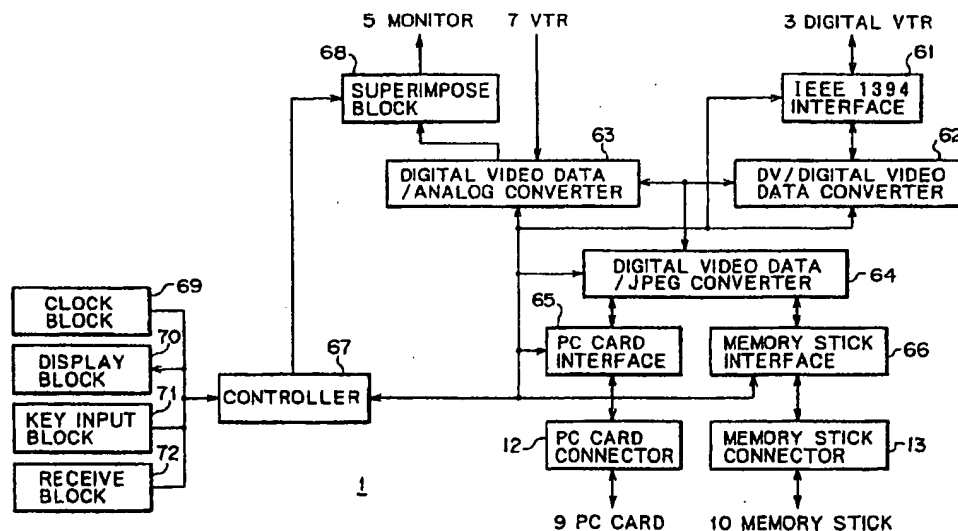
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(54) Image processing apparatus, image processing method and information providing medium

(57) An image processing apparatus and method for facilitating a still image copying operation between a plurality of storage media. Under the control of the controller 67, a DV/digital video data converter 62, a digital video data/analog converter 63, and a digital video data/JPEG converter 64 convert input digital video data into a still image, convert the still image into digital video data or an analog video signal, and convert an input

analog video signal into a still image. Under the control of the controller 67, a PC card interface 65 stores the still image into a PC card 9 and reads the still image therefrom. Under the control of the controller 67, a Memory Stick interface 66 stores a still image into a Memory Stick 10 and reads the still image therefrom.

FIG. 9



Description

BACKGROUND OF THE INVENTION

[0001] The present invention relates generally to an image processing apparatus, an image processing method, and an information providing medium and, more particularly, to an image processing apparatus, an image processing method, and an information providing medium that perform conversion between digital video data and analog video signal.

[0002] With popularization of combination digital camera/recorder systems, signal converters for performing conversion between digital video data and analog video signal have started to be used. These signal converters convert digital video data into analog video signals and vice versa.

[0003] Image processors such as personal computers incorporating a video capture board are in use to store still images included in digital video data or analog video signals.

[0004] On the other hand, digital still cameras often use a PCMCIA (Personal Computer Memory Card International Association) card or simply a PC card, a flash memory chip, or a Memory Stick (trademark of Sony Corporation), a recordable IC memory card, as a storage medium for storing still images.

SUMMARY OF THE INVENTION

[0005] However, storing still images included in digital video data or analog video signals into a PC card, a flash memory chip, or a Stick Memory (trademark) requires an image processor such as a personal computer on which these storage media can be loaded, requiring a very cumbersome task.

[0006] Copying a still image stored in a Memory Stick for example into a PC card requires a personal computer to be adapted to load these storage media, requiring a yet cumbersome task.

[0007] It is therefore an object of the present invention to provide an image processing apparatus and an image processing method that facilitate the storing of still images included in digital video data or analog video signals into storage media and the copying of the still images between these storage media.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] These and other objects of the invention will be seen by reference to the description, taken in connection with the accompanying drawing, in which:

FIG. 1 is a schematic diagram illustrating a signal converter 1 practiced as one embodiment of the invention;

FIG. 2 is a front view of the signal converter 1 with its cover 11 opened;

FIG. 3 is a right side view of the signal converter 1 with its cover 11 opened;

FIG. 4 is a front view of the signal converter 1 with its cover 11 closed;

FIG. 5 is the right side view of the signal converter 1 with its cover 11 closed;

FIG. 6 is a top view of the signal converter 1;

FIG. 7 is a perspective view illustrating a state in which a PC card connector 12 and a Memory Stick connector 13 are mounted on a board 31;

FIG. 8 shows a front view of a remote commander 2;

FIG. 9 is a block diagram illustrating the configuration of the signal converter 1;

FIG. 10 exemplarily illustrates a screen displayed by the signal converter 1 onto a monitor 5;

FIG. 11 exemplarily illustrates another screen displayed by the signal converter 1 onto the monitor 5;

FIG. 12 exemplarily illustrates still another screen displayed by the signal converter 1 onto the monitor 5;

FIG. 13 exemplarily illustrates yet another screen displayed by the signal converter 1 onto the monitor 5;

FIG. 14 exemplarily illustrates a different screen displayed by the signal converter 1 onto the monitor 5;

FIG. 15 exemplarily illustrates still different screen displayed by the signal converter 1 onto the monitor 5;

FIG. 16 exemplarily illustrates yet different screen displayed by the signal converter 1 onto the monitor 5;

FIG. 17 exemplarily illustrates another screen displayed by the signal converter 1 onto the monitor 5;

FIG. 18 exemplarily illustrates still another screen displayed by the signal converter 1 onto the monitor 5;

FIG. 19 is a flowchart for describing the processing of displaying an icon 101 indicative of a Memory Stick 10 or an icon 102 indicative of a PC card 9 onto the monitor 5;

FIG. 20 is a flowchart for describing the processing of reproducing an image stored in the Memory Stick 10 or the PC card 9;

FIG. 21 is a flowchart for describing the processing of storing a still image into the Memory Stick 10 or the PC card 9;

FIG. 22 is a flowchart for describing the processing of copying a still image from the Memory Stick 10 to the PC card 9; and

FIG. 23 is a flowchart for describing the processing of lighting a LED 14.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0009] This invention will be described in further

detail by way of example with reference to the accompanying drawings.

[0010] Now referring to FIG. 1, a signal converter 1 is remotely controlled by a remote commander 2. The signal converter 1 is connected to a video camera integrated digital VTR (Video Tape Recorder) 3 through an IEEE (Institute of Electrical and Electronics Engineers) 1394 serial bus 4. The signal converter 1 outputs digital video data of DV (Digital Video) VCR (Video Cassette Recorder) format to the digital VTR 3 and receives therefrom digital video data of DV VCR format. That is to say, the IEEE 1394 is used as a digital interface between the signal converter 1 and the digital VTR 3.

[0011] The signal converter 1 outputs an analog video signal to a monitor 5 through a cable 6 and receives an analog video signal from a VTR 7 through a cable 8.

[0012] The signal converter 1 converts digital video data received from the digital VTR 3 into an analog video signal and outputs it to the monitor 5. Or the signal converter 1 converts an analog video signal received from the VTR 7 into digital video data and outputs the same to the digital VTR 3. Or the signal converter 1 outputs an analog video signal received from the VTR 7 directly to the monitor 5.

[0013] The signal converter 1 is loaded with a PC card 9 or a Memory Stick 10 incorporated with a predetermined memory, converts the inputted digital video data or analog video signal into a still image according to a predetermined operation by a user, and stores the obtained still image into the loaded PC card 9 or the Memory Stick 10.

[0014] The signal converter 1 converts a still image stored in the loaded PC card 9 or Memory Stick 10 into an analog video signal and output it to the monitor 5. The signal converter 1 converts a still image stored in the loaded PC card 9 or Memory Stick 10 into digital video data and outputs the same to the digital VTR 3.

[0015] The signal converter 1 copies a still image stored in the loaded PC card 9 to the loaded Memory Stick 10 according to a predetermined operation by the user. Conversely, the signal converter 1 copies a still image stored in the loaded Memory Stick 10 to the loaded PC card 9.

[0016] The voice processing to be executed by the signal converter 1 will be skipped.

[0017] Referring to FIGS. 2 and 3, a PC card connector 12 is loaded with the PC card incorporating a predetermined memory. A Memory Stick connector 13 is inserted with the Memory Stick 10. The Memory Stick 10 loaded in the Memory Stick connector 13 may be unloaded when the user presses it again.

[0018] A LED (Light Emitting Diode) 14 displays a state of the Memory Stick 10 loaded on the Memory Stick connector 13; for example, whether data are written to or an error occurred on the Memory Stick 10. A LED 15 goes on when an analog video signal is inputted from the VTR 7 into the signal converter 1. A LED 16

goes on when digital video data are inputted from the digital VTR 3 into the signal converter 1. A LED 17 displays a state of the loaded PC card; for example, whether data are written to or an error occurred on the PC card 9 loaded on the PC card connector 12.

[0019] Referring to FIGS. 4 and 5, when a cover 11 of the signal converter 1 is closed, the PC card connector 12 is covered (or the opening is blocked), disabling the user to take the PC card 9 out of the PC card connector 12 or load the PC card 9 on the PC card connector 12.

[0020] On the other hand, even if the cover 11 is closed, a hole 18 provided in the cover 11 allows the user to take the Memory Stick 10 out of the Memory Stick connector 13 (when the loaded Memory Stick 10 is pressed into the Memory Stick connector 13, the top of the Memory Stick 10 projects from the Memory Stick connector 13 to allow the user to hold the top between fingers) or the Memory Stick 10 can be loaded into the Memory Stick connector 13.

[0021] Referring to FIG. 6, the signal converter 1 is arranged on the top side thereof with a record button 21, a capture button 22, an index button 23, a play button 24, an analog input select button 25, and a DV input select button 26. The record button 21 is operated when storing a still image included in the input analog video signal or digital video data into the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0022] The capture button 22 is operated to temporarily stop (or capture) a moving image displayed on the monitor 5 before storing a still image included in the inputted analog video signal or digital video data into the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0023] The index button 23 is operated when displaying on the monitor 5 a plurality of still-image thumbnail pictures stored in the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0024] The play button 24 is operated when displaying on the monitor 5 a still image stored in the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0025] The analog input select button 25 is operated when inputting an analog video signal from the VTR 7 to the signal converter 1. The DV input select button 26 is operated when inputting digital video data from the digital VTR 3 to the signal converter 1.

[0026] The turn on/off states of the LED 14, LED 15, LED 16, and LED 17 can also be confirmed from the top side of the signal converter 1.

[0027] Referring to FIG. 7, there is shown a state in which the PC card connector 12 and the Memory Stick connector 13 are mounted on a board 31 on which predetermined components of the signal converter 1 are mounted. As shown, the PC card connector 12 is fixed to the upper surface of the board 31. The Memory Stick

connector 13 is fixed to the lower surface of the board 31, the opening through which the Memory Stick 10 is loaded being oriented in the same direction as the opening of the PC card connector 12 through which the PC card 9 is loaded, the vertical centers of these openings being in general alignment with each other.

[0028] Because the openings of the PC card connector 12 and the Memory Stick connector 13 are oriented in the same direction and the vertical centers of the openings of these connectors are in general alignment with each other, the areas on the board 31 necessary for mounting these connectors are minimized, thereby reducing the signal converter 1 in physical size.

[0029] Referring to FIG. 8, the remote commander 2 has a record button 41, a capture button 42, an index button 43, a play button 44, an analog input select button 45, and a DV input select button 46. The record button, corresponding to the record button 21 of the signal converter 1, is operated to store a still image included in the inputted analog video signal or digital video data into the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0030] The capture button 42, corresponding to the capture button 22 of the signal converter 1, is operated to temporarily stop (or capture) a moving image displayed on the monitor 5 when storing a still image included in the inputted analog video signal or digital video data into the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0031] The index button 43, corresponding to the index button 23 of the signal converter 1, is operated to display on the monitor 5 an index of a still image stored in the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0032] The play button 44, corresponding to the play button 24 of the signal converter 1, is operated to display on the monitor 5 a still image stored in the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12.

[0033] The analog input select button 45, corresponding to the analog input select button 25 of the signal converter 1, is operated to input an analog video signal from the VTR 7 to the signal converter 1. The DV input select button 46, corresponding to the DV input select button 26 of the signal converter 1, is operated to input digital video data from the digital VTR 3 to the signal converter 1.

[0034] Referring to FIG. 9, an IEEE 1394 interface 61 is an input/output interface connected to one end of the IEEE 1394 serial bus 4, the other end thereof being connected to the digital VTR 3. Under the control of a controller 67, the IEEE 1394 interface 61 supplies digital video data from the digital VTR 3 to a DV/digital video data converter 62 through the IEEE 1394 serial bus 4 and digital video data from the DV/digital video data

converter to the digital VTR 3 through the IEEE 1394 serial bus.

[0035] When the digital video data are inputted from the digital VTR 3 into the signal converter 1, the DV/digital video data converter 62, under the control of the controller 67, converts the digital video data having DV VCR format supplied from the IEEE 1394 interface 61 into internal digital video data and supplies the same to a digital video data/analog converter 63 and a digital video data/JPEG (Joint Photographic Experts Group) converter 64. When an analog video signal is inputted from the VTR 7 into the signal converter 1, the DV/digital video data converter 62 converts the internal digital video data supplied from the digital video data/analog converter 63 into digital video data of DV VCR format, supplying it to the IEEE 1394 interface 61.

[0036] The DV/digital video data converter 62 converts the internal digital video data supplied from the digital video data/JPEG converter 64 into digital video data of DV VCR format and supplies it to the IEEE 1394 interface 61.

[0037] When digital video data are inputted from the digital VTR 3 into the signal converter 1, the digital video data/analog converter 63, under the control of the controller 67, converts the internal digital video data supplied from the DV/digital video data converter 62 into an analog video signal and supplies it to a superimpose block 68. When an analog video signal is inputted in the signal converter 1, the digital video data/analog converter 63 converts the analog video signal supplied from the VTR 7 into internal digital video data and supplies the same to the DV/digital video data converter 62 and the digital video data/JPEG converter 64.

[0038] Under the control of the controller 67, the digital video data/JPEG converter 64 converts the internal digital video data supplied from the DV/digital video data converter 62 or the digital video data/analog converter 63 into JPEG still image data and outputs the same to a PC card interface 65 and a Memory Stick interface 66. Also, the digital video data/JPEG converter 64 converts the JPEG still image data supplied from the PC card interface 65 or the Memory Stick interface 66 into internal digital video data and supplies the same to the DV/digital video data converter 62 and the digital video data/analog converter 63.

[0039] Under the control of the controller 67, the PC card interface 65 stores the JPEG still image data supplied from the digital video data/JPEG converter 64 into the PC card 9 loaded in the PC card connector 12. Also, the PC card interface 65 reads the JPEG still image data from the PC card 9 loaded in the PC card connector 12 and outputs the read data to the digital video data/JPEG converter 64.

[0040] Under the control of the controller 67, the Memory Stick interface 66 stores the JPEG still image data supplied from the digital video data/JPEG converter 64 into the Memory Stick 10 loaded in the Memory Stick connector 13. Also, the Memory Stick interface

66 reads the JPEG still image data from the Memory Stick 10 loaded in the Memory Stick connector 13 and outputs the read data to the digital video data/JPEG converter 64.

[0041] On the basis of the signals supplied from a key input block 71 or a receive block 72, the controller 67 controls the IEEE 1394 interface, the DV/digital video data converter 62, the digital video data/analog converter 63, the digital video data/JPEG converter 64, the PC card interface 65, the Memory Stick interface 66, the superimpose block 68, and a display block 70.

[0042] The superimpose block 68, under the control of the controller 67, superimposes a predetermined icon or character signal indicative of the state of the signal converter 1 onto an analog video signal supplied from the digital video data/analog converter 63 and outputs the resultant signal to the monitor 5 through the cable 6.

[0043] The clock block 69 supplies the data indicative of date and time to the controller 67. The display block 70, composed of the LED 14, the LED 15, the LED 16, and LED 17, and a LED drive block for driving these LEDs, turns off these LEDs, and turns on or flashes them in a predetermined color under the control of the controller 67.

[0044] The key input block 71 is composed of the record button 21, the capture button 22, the index button 23, the play button 24, the analog input select button 25, and the DV input select button 26 and supplies signals associated with the operations of these buttons to the controller 67. The receive block 72 receives in infrared radiation signal from the remote commander 2 and supplies a signal corresponding to a flash pattern (corresponding to each of the operations of the record button 41, the capture button 42, the index button 43, the play button 44, the analog input select button 45, and the DV input select button 46) of the infrared radiation signal to the controller 67.

[0045] Referring to FIG. 10, there is shown a screen, by way of example, which is displayed on the monitor 5 by the signal converter 1 when the play button 24 is pressed and the Memory Stick 10 storing no still image data is loaded in the Memory Stick connector 13. The signal converter 1 displays an icon 101 indicative of the Memory Stick 10 in the upper right corner of the screen on the monitor 5 and character string "MEMORY REPRODUCTION" below the icon 101. Also, the signal converter 1 displays, to the left of the icon 101, numbers "0/0" for example indicative that the Memory Stick 10 loaded in the Memory Stick connector 13 stores no still image data. The right-hand number indicates the number of still images stored in the loaded Memory Stick 10, while the left-hand number indicates the number of the current displayed still image among those stored in the Memory Stick 10 (in this example, no still image is stored in the Memory Stick 10 and therefore zero is shown on the blue background for example of the monitor screen).

[0046] Referring to FIG. 11, there is shown a

screen, by way of example, which is displayed on the monitor 5 by the signal converter 1 when the index button 23 is pressed and the PC card 9 storing no still image data is loaded in the PC card connector 12. The signal converter 1 displays an icon 102 indicative of the PC card 9 in the upper right corner of the screen of the monitor 5 and character strings "INDEX REPRODUCTION" below the icon 102. Also, the signal converter 1 displays, to the left of the icon 102, numbers "0/0" for example indicative that the loaded PC card 9 stores no still image. These numbers have the same meaning as those of FIG. 10.

[0047] Referring to FIG. 12, there is shown a screen, by way of example, which is displayed on the monitor 5 by the signal processor 1 when the play button 24 is pressed and the Memory Stick 10 storing predetermined still image data is loaded in the Memory Stick connector 13. The signal converter 1 displays a predetermined image stored in the Memory Stick 10 onto the monitor 5.

[0048] As with the case described with reference to FIG. 10, the signal converter 1 displays the icon 101 indicative of the Memory Stick 10 in the upper right corner of the screen of the monitor 5 and character string "MEMORY REPRODUCTION" below the icon 101.

[0049] Also, the signal converter 1 displays, to the left of the icon 101, numbers "10/12" for example indicative that the loaded Memory Stick 10 stores 12 still images and the still image currently displayed on this screen is number "10". In addition, the signal converter 1 displays character string "SFN" in the upper center of the screen, "SFN" denoting the resolution of the currently displayed image. Furthermore, the signal converter 1 displays, in the upper left corner of the screen, a file name of the currently displayed image and an icon 103 indicative that the file of the currently display image is protected against deletion.

[0050] Also, the signal converter 1 displays, in the lower left corner of the screen, a date on which the still image data was stored in the Memory Stick 10. The user can set the signal converter 1 so that this date is not displayed.

[0051] Referring to FIG. 13, there shown a screen, by way of example, which is displayed by the signal converter 1 onto the monitor 5 when the index button 23 is pressed and the Memory Stick 10 storing predetermined still images is loaded in the Memory Stick connector 13. The signal converter 1 generates the predetermined number of thumbnail images from the images stored in the Memory Stick 10 and displays the generated thumbnail images and the files names corresponding to these thumbnail images onto the screen of the monitor 5. The signal converter 1 also displays, in the lower right corner of the screen, the icon 101 indicative of the Memory Stick 10.

[0052] The signal converter 1 displays, to the left of the icon 101 on the screen of the monitor 5, numbers "120/120" for example indicative that the Memory Stick

10 loaded in the Memory Stick connector 13 stores 120 still images and the last thumbnail image (the image having the greatest number) displayed on the screen of the monitor 5 is number "120". Further, the signal converter 1 displays a file name corresponding to the last image displayed on the monitor 5 in the lower left corner of the screen.

[0053] Referring to FIG. 14, there is shown a screen, by way of example, to be displayed by the signal converter 1 when the Memory Stick 10 is pulled out of the Memory Stick connector 13 while an image stored in the Memory Stick 10 loaded in the Memory Stick connector 13 is reproduced. If this happens, the signal converter 1 displays at the center of the screen, an icon 104 indicative of the pull-out.

[0054] Referring to FIG. 15, there is shown a screen, by way of example, to be displayed by the signal converter 1 on the monitor 5 when the Memory Stick 10 is pulled out of the Memory Stick connector 13 and, at the same time, the PC card 9 is pulled out of the PC card connector 12 while an image stored in the Memory Stick 10 loaded in the Memory Stick connector 13 or an image stored in the PC card 9 loaded in the PC card connector 12 is reproduced. The signal converter 1 displays, side by side in the center of the screen, the icon 104 indicative of the pull-out of the Memory Stick 10 and an icon indicative of the pull-out of the PC card 9.

[0055] Referring to FIG. 16, there is shown a screen, by way of example, to be displayed by the signal converter 1 on the monitor 5 when the data corresponding to a still image included in an analog video signal are stored in the Memory Stick 10 loaded in the Memory Stick connector 13 when the record button 21 is pressed by the user. The signal converter 1 displays the icon 101 indicative of the Memory Stick 10 in the upper right corner of the screen and character string "ANALOG INPUT" below the icon 101.

[0056] At the same time, the signal converter 1 displays, to the left of the icon 101, numbers "80/120" indicative of the state of the storage of the images in the Memory Stick 10 loaded in the Memory Stick connector 13. Number "120" indicates the maximum number of still images (generated at a predetermined data compression ratio) that can be stored in the Memory Stick 10 loaded in the Memory Stick connector 13. Number "80" indicates the number of images currently stored in the Memory Stick 10 loaded in the Memory Stick connector 13. The signal converter 1 displays, at the upper center of the screen, character "STANDARD" indicative of the still image compression ratio.

[0057] Further, the signal converter 1 displays, in the lower left corner of the screen, a date on which the still image file was stored in the Memory Stick 10. The user can set the signal converter 1 so that the date is not displayed.

[0058] Referring to FIG. 17, there is shown on a screen, by way of example, to be displayed by the signal converter 1 on the monitor 5 when the record button 21

is pressed and a still image included in digital video data is stored in the PC card 9 connected to the PC card connector 12. The signal converter 1 displays, in the upper right corner of the screen of the monitor 5, the icon 102 indicative of the PC card 9 and character string "DV INPUT" below the icon 102.

[0059] At the same time, the signal converter 1 displays, to the left of the icon 102, numbers "30/80" indicative of the state of the storage of images in the PC card 9 loaded in the PC card connector 12. The meanings of these numbers are the same as those shown in FIG. 16.

[0060] The signal converter 1 displays, at the upper center of the screen, character string "SFN" indicative of the data compression ratio of the still image and, in the lower left corner of the screen, the date on which the still image file was stored in the PC card 9. The user can set the signal converter 1 so that the date is not displayed.

[0061] Referring to FIG. 18, there is shown a screen, by way of example, to be displayed by the signal converter 1 on the monitor 5 when a still image file stored in the PC card 9 loaded in the PC card connector 12 is copied into the Memory Stick 10 loaded in the Memory Stick connector 13. The signal converter 1 displays, in the center of the screen, the icon 102 indicative of the PC card 9, the icon 101 indicative of the Memory Stick 10, and an icon 106 indicative that the still image file is copied from the PC card 9 to the Memory Stick 10.

[0062] The following describes, with reference to the flowchart shown in FIG. 19, the processing for displaying on the monitor 5 by the controller 67 of the signal converter 1 the icon 101 indicative of the Memory Stick 10 or the icon 102 indicative of the PC card 9 when the play button 24 or the index button 23 is pressed and the Memory Stick 10 or the PC card 9 is loaded. In step S11, the controller 67 determines, on the basis of a signal supplied from the Memory Stick interface 66, whether the Memory Stick 10 has been loaded in the Memory Stick connector 13. If the Memory Stick 10 is found loaded, then, in step S12, the controller 67 causes the superimpose block 68 to display the icon 101 indicative of the Memory Stick 10. Then, back to step S11, the controller 67 repeats the icon displaying processing.

[0063] In step S11, if the Memory Stick 10 is found not loaded in the Memory Stick connector 13, then, in step S13, the controller 67 determines, on the basis of a signal supplied from the PC card interface 65, whether the PC card 9 has been loaded in the PC card connector 12. If the PC card 9 is found loaded, then, in step S14, the controller 67 causes the superimpose block 68 to display the icon 102 indicative of the PC card 9. Then, back to step S11, the controller 67 repeats the icon displaying processing.

[0064] If the PC card is not found loaded in step S13, it indicates that neither the Memory Stick 10 nor the PC card 9 has been loaded. Then, in step S15, the controller 67 causes the superimpose block 68 to display the icon 104 indicative that the Memory Stick 10 is

not loaded and the icon 105 indicative that the PC card is not loaded. Then, back to step S11, the controller 67 repeats the icon display processing until the Memory Stick 10 or the PC card 9 is loaded.

[0065] Thus, when the play button 24 or the index button 23 has been pressed and the Memory Stick 10 or the PC card 9 is loaded, the signal converter 1 displays the icon corresponding to the loaded Memory Stick 10 or the PC card 9 onto the screen of the monitor 5.

[0066] The following describes, with reference to the flowchart shown in FIG. 20, the processing of reproducing an image stored in the Memory Stick 10 loaded in the Memory Stick connector 13 or the PC card 9 loaded in the PC card connector 12. In step S31, the controller 67 of the signal converter 1 determines, on the basis of a signal supplied from the Memory Stick interface 66 and a signal supplied from the PC card interface 65, whether the Memory Stick 10 is loaded in the Memory Stick connector 13 or the PC card 9 is loaded in the PC card connector 12. If neither the Memory Stick 10 nor the PC card 9 is found loaded, then, in step S32, the controller 67 causes the superimpose block 68 to display the icon 104 indicative that the Memory Stick 10 is not loaded and the icon 105 indicative that the PC card 9 is not loaded. Then, back to step S31, the controller 67 repeats the processing until the Memory Stick 10 or the PC card 9 is loaded.

[0067] If, in step S31, the Memory Stick 10 or the PC card 9 is found loaded, then, in step S33, the controller 67 determines, on the basis of a signal supplied from the Memory Stick interface 66, whether the Memory Stick 10 is loaded in the Memory Stick connector 13. If the Memory Stick 10 is found loaded, then, in step S34, the controller 67 causes the superimpose block 68 to display the icon 101 indicative that the Memory Stick 10 is loaded.

[0068] In step S35, the controller 67 causes the Memory Stick interface 66 to read from the Memory Stick 10 the number of images stored therein and the data such as image data compression ratio. In step S36, the controller 67 causes the superimpose block 68 to display the number of images stored in the Memory Stick 10 and the data such as image data compression ratio.

[0069] In step S37, the controller 67 causes the Memory Stick interface 66 to read predetermined still image data from the Memory Stick 10 and supply the data to the digital video data/JPEG converter 64. The digital video data/JPEG converter 64 converts the supplied data having JPEG format into internal digital video data and supplies the resultant data to the digital video data/analog converter 63. The digital video data/analog converter 63 converts the supplied internal digital video data into an analog video signal and supplies it to the monitor 5 to display the predetermined still image.

[0070] In step S38, the controller 67 determines, on the basis of a signal supplied from the key input block 71, whether the play button 24 of the key input block 71

has been pressed. If the play button 24 is found pressed, it indicates that an image stored in the PC card 9 is reproduced, so that, in step S39, the controller 67 determines, on the basis of a signal supplied from the PC card interface 65, whether the PC card 9 is loaded in the PC card connector 12.

[0071] If, in step S39, the PC card 9 is found not loaded, then, in step S40, the controller 67 determines, on the basis of a signal supplied from the Memory Stick interface 66, whether the Memory Stick 10 has been pulled out of the Memory Stick connector 13. If the Memory Stick 10 is found pulled out, the controller 67 causes the superimpose block 68 to display the icon 104 indicative of the pull-out. Then, back to step S31, the controller 67 repeats the image reproduction processing.

[0072] If, in step S40, the Memory Stick 10 is found not pulled out, it indicates that an image stored in the Memory Stick 10 is reproduced, so that, back to step S37, the controller 67 repeats the image reproduction processing.

[0073] If, in step S38, the play button 24 is found not pressed, it indicates that an image stored in the Memory Stick 10 is successively reproduced, so that the controller 67 skips the processing of step S39 and executes the processing of step S40.

[0074] If, in step S39, the PC card 9 is found loaded, and if, in step S33, the Memory Stick 10 is found not loaded, it indicates that an image stored in the PC card 9 is reproduced. Then, in step S42, the controller 67 causes the superimpose block 68 to display the icon 102 indicative that the PC card 9 is loaded.

[0075] In step S43, the controller 67 causes the PC card interface 65 to read the number of images stored in the PC card 9 and the data such as image data compression ratio. In step S44, the controller 67 causes the superimpose block 68 to display the number of images stored in the PC card 9 and the data such as image data compression ratio.

[0076] In step S45, the controller 67 causes the PC card interface 65 to read the predetermined still image data from the PC card 9 and supplies the data to the digital video data/JPEG converter 64. The digital video data/JPEG converter 64 converts the still image data having JPEG format supplied from the PC card interface 65 into internal digital video data and supplies the same to the digital video data/analog converter 63. The digital video data/analog converter 63 converts the internal digital video data into an analog video signal and supplies it to the monitor 5 to display the predetermined still image.

[0077] In step S46, the controller 67 determines, on the basis of a signal supplied from the key input block 71, whether the play button 24 of the key input block 71 has been pressed. If the play button 24 is found pressed, it indicates that an image stored in the Memory Stick 10 is reproduced. Therefore, in step S47, the controller 67 determines, on the basis of a signal supplied

from the Memory Stick interface 66, whether the Memory Stick 10 is loaded in the Memory Stick connector 13.

[0078] If, in step S47, the Memory Stick 10 is found not loaded, then, in step S48, the controller 67 determines, on the basis of a signal supplied from the PC card interface 65, whether the PC card 9 has been pulled out of the PC card connector 12. If the PC card 9 is found pulled out, the controller 67 causes the superimpose block 68 to display the icon 105 indicative of the pull-out. Then, back to step S31, the controller 67 repeats the image reproduction processing.

[0079] If, in step S48, the PC card 9 is found not pulled out, it indicates that an image stored in the PC card 9 is reproduced, so that, back to step S45, the controller 67 repeats the image reproduction processing.

[0080] If, in step S46, the play button 24 is found not pressed, it indicates that an image stored in the PC card 9 is successively reproduced, so that the controller 67 skips the processing of step S47 and executes the processing of step S48.

[0081] If, in step S47, the Memory Stick 10 is found not loaded, it indicates that an image stored in the Memory Stick 10 is reproduced. Then, in step S34, the controller 67 executes the processing for displaying the still image stored in the Memory Stick 10.

[0082] Thus, the signal converter 1 displays on the monitor 5 predetermined images stored in the Memory Stick 10 or the PC card 9. When the play button 24 is pressed while a still image stored in the Memory Stick 10 is displayed on the monitor 5, a predetermined image stored in the PC card 9 can also be displayed on the monitor 5. Conversely, when the play button 24 is pressed while a still image stored in the PC card 9 is displayed on the monitor 5, a predetermined still image stored in the Memory Stick 10 can also be displayed on the monitor 5.

[0083] Further, in the like processing, the signal converter 1 can convert predetermined still image data stored in the Memory Stick 10 or the PC card 9 into DV VCR digital video data and supply the same to the digital VTR 3.

[0084] The following describes, with reference to the flowchart shown in FIG. 21, the processing of storing a still image included in the digital video data supplied from the digital VTR 3 or a still image included in an analog video signal supplied from the VTR 7 into the Memory Stick 10 or the PC card 9. In step S71, the controller 67 of the signal converter 1 determines whether the capture button 22 of the key input block 71 has been pressed. If the capture button 22 is found pressed, then, in step S72, the controller determines, on the basis of a signal supplied from the digital video data/analog converter 63 or the DV/digital video data converter 62, whether an image displayed by the digital video data/analog converter 63 on the monitor 5 is frozen.

[0085] If, in step S72, the image is found not frozen, then, in step S73, the controller 67 causes the digital video data/analog converter 63 or the DV/digital video

data converter 62 to freeze the image displayed on the monitor. Then, the controller 67 takes up the processing of step S75.

[0086] If, in step S72, the image is found frozen, then, in step S74, the controller 67 causes the digital video data/analog converter 63 or the DV/digital video data converter 62 to unfreeze the image. Then, the controller 67 takes up the processing of step S75.

[0087] If, in step S71, the capture button 22 is found not pressed, then the controller 67 skips the processing of steps S72 through S74 and takes up the processing of step S75.

[0088] In step S75, the controller 67 determines whether the record button 21 of the key input block 71 has been pressed. If the record button 21 is found pressed, then, in step S76, the controller causes the digital video data/JPEG converter 64 to convert the internal digital video data supplied from the digital video data/analog converter 63 or the DV/digital video data converter 62 into JPEG data and supply the same to the PC card interface 65 and the Memory Stick interface 66. In step S77, on the basis of the data supplied from the clock block 69 indicative of date and time, the controller 67 supplies data indicative of date and time to the PC card interface 65 and the Memory Stick interface 66.

[0089] In step S78, the controller 67 determines, on the basis of a signal supplied from the Memory Stick interface 66, whether the Memory Stick 10 is loaded in the Memory Stick connector 13. If the Memory Stick 10 is found loaded, the controller 67 causes the Memory Stick interface 66 to write the still image data and the date and time data to the Memory Stick 10. In step S80, the controller 67 causes the superimpose block 68 to display the predetermined icon and data. Then, back to step S71, the controller 67 repeats the still image storage processing.

[0090] If, in step S78, the Memory Stick 10 is found not loaded, then, in step S81, the controller 67 determines, on the basis of a signal supplied from the PC card interface 65, whether the PC card 9 is loaded in the PC card connector 12. If the PC card 9 is found loaded, then, in step S82, the controller causes the PC card interface 65 to write the still image data and the date and time data to the PC card 9. In step S83, the controller 67 causes the superimpose block 68 to display the predetermined icon and data on the monitor 5. Then, back to step S71, the controller 67 repeats the still image storage processing.

[0091] If, in step S81, the PC card 9 is found not loaded, it indicates that the data cannot be written. Therefore, in step S84, the controller 67 causes the superimpose block 68 to display a predetermined error message on the monitor 5. Then, back to step S71, the controller 67 repeats the still image storage processing.

[0092] If, in step S75, the record button is found not pressed, then, back to step S71, the controller 67 repeats the still image storage processing.

[0093] Thus, operating the capture button 22 allows

the signal converter 1 to freeze an image to be displayed on the monitor 5. If the capture button 22 is not pressed but the record button 21 is pressed, then the signal converter 1 does not freeze an image to be displayed on the monitor 5 but stores the image into the Memory Stick 10 or the PC card 9.

[0094] Consequently, assume for example that the VTR 7, instead of the monitor 5, be connected to the analog video signal output terminal of the signal converter 1, the signal converter 1 convert the digital video data supplied from the digital VTR 3 into an analog video signal, and the VTR 7 record this video signal to a magnetic tape loaded on the VTR 7. Then, pressing only the record button 21 allows the signal converter 1 to store the predetermined still image into the Memory Stick 10 or the PC card 9 without affecting the image being recorded by the VTR 7.

[0095] The following describes, with reference to the flowchart shown in FIG. 22, the processing of copying still image data stored in the Memory Stick 10 into the PC card 9. In step S101, the user loads the Memory Stick 10 in which copied still image data are stored into the Memory Stick connector 13 to reproduce an image corresponding to the data to be copied. In step S102, the controller 67 of the signal converter 1 determines, on the basis of a signal supplied from the receive block 72, whether the copy button of the remote commander 2 has been pressed. If the copy button is found not pressed, then, back to step S101, the controller 67 repeats the above-mentioned processing until the copy button is pressed.

[0096] If, in step S102, the copy button is found pressed, then, in step S103, the controller 67 determines, on the basis of a signal supplied from the PC card interface 65, whether the PC card 9 is loaded in the PC card connector 12. If the PC card 9 is found loaded, then, in step S104, the controller 67 causes the superimpose block 68 to display a screen of copy acknowledgment.

[0097] In step S105, the controller 67 determined, on the basis of a signal supplied from the receive block 72, whether the copy button of the remote commander 2 has been pressed. If the copy button is found pressed, then, in step S106, the controller causes the Memory Stick interface 66 to read the data corresponding to the reproduced image from the Memory Stick 10 and causes the PC card interface 65 to write these data to the PC card 9.

[0098] In step S107, the controller 67 causes the superimpose block 68 to display the icon shown in FIG. 18 indicative that copy is being executed. Then, back to step S101, the controller 67 repeats the copy processing.

[0099] If, in step S105, the copy button is found not pressed, then, in step S108, the controller determines, on the basis of a signal supplied from the receive block 72, whether a button other than the copy button on the remote commander 2 has been pressed. If no button

other than the copy button is found not pressed, it indicates that none of the buttons on the remote commander 2 has been pressed. Therefore, back to step S105, the controller 67 repeats the above-mentioned processing until the remote commander 2 is operated.

[0100] If, in step S108, a button other than the copy button is found pressed, it indicates that the copy operation has been canceled. Then, back to step S101, the controller 67 repeats the image data copy processing.

[0101] If, in step S103, the PC card 9 is found not loaded in the PC card connector 12, then, in step S109, the controller 67 causes the superimpose block 68 to display a screen for prompting the loading of the PC card 9. Then, back to step S101, the controller 67 repeats the image data copy processing.

[0102] Thus, the signal converter 1 copies predetermined still image data from the Memory Stick 10 to the PC card 9. The processing for copying predetermined still image data from the PC card 9 to the Memory Stick 10 is executed in generally the same manner except that copy is made in the opposite direction. Also, the data of a plurality of still images being index-reproduced may be copied.

[0103] The following describes, with reference to the flowchart shown in FIG. 23, the processing of turning on the LED 14 indicative of the state of the Memory Stick 10 loaded in the Memory Stick connector 13. In step S131, the controller 67 of the signal converter 1 determines, on the basis of a signal supplied from the Memory Stick interface 66, whether an error has occurred on the Memory Stick 10. If an error is found, then, in step S132, the controller 67 causes the LED 14 of the display block 70 to flash red. Then, back to step S131, the controller repeats the processing.

[0104] If, in step S131, no error is found, then, in step S133, the controller 67 determines, on the basis of a signal supplied from the Memory Stick interface 66, whether still image data are written to the Memory Stick 10 loaded in the Memory Stick connector 13. If the data are found being written, then, in step S134, the controller causes the LED 14 to flash red. Then, back to step S131, the controller repeats the processing.

[0105] If, in step S133, the still image data are not found written to the Memory Stick 10, then, in step S135, the controller 67 determines whether a still image stored in the Memory Stick 10 is being reproduced. If the still image is found being reproduced, then, in step S136, the controller 67 determines whether the still image data stored in the Memory Stick 10 are being read.

[0106] If, in step S136, the still image data are found not being read, then, in step S137, the controller 67 turns on green the LED 14 of the display block 70. Then, back to step S131, the controller 67 repeats the processing.

[0107] If, in step S136, the still image data stored in the Memory Stick 10 are found being read, then, in step S138, the controller flashes green the LED 14. Then,

back to step S131, the controller 67 repeats the processing.

[0108] If, in step S135, the still image stored in the Memory Stick 10 is found not being reproduced, then, in step S139, the controller turns off the LED 14. Then, back to step S131, the controller 67 repeats the processing.

[0109] Thus, the controller 67 turns on or off the LED 14 according to the state of the Memory Stick 10 loaded in the Memory Stick connector 13. Likewise, the controller 67 turns on or off the LED 17 according to the state of the PC card 9 loaded in the PC card connector 12.

[0110] In the above description, the PC card 9 or the Memory Stick 10 are loaded on the signal converter 1. It will be apparent to those skilled in the art that other storage media such as SmartMedia (trademark) and CompactFlash (trademark) may also be used.

[0111] Also, in the above description, digital video data of DV VCR format are inputted in the signal converter 1 and these data are outputted therefrom. It will be apparent to those skilled in the art that the digital video data may also be compressed by MPEG (Moving Picture Experts Group) format. It will be also apparent to those skilled in the art that the digital video data may be stored on a disc or a solid memory in addition to a magnetic tape or inputted/outputted via a network or a communication satellite.

[0112] In the above description, when copying a still image from the PC card 9 to the Memory Stick 10, the signal converter 1 displays the icon 101, the icon 102, and the icon 106 in the center of the screen of the monitor 5. It will be apparent to those skilled in the art that, when copying a moving image from the digital VTR 3 to the VTR 7 for example, the signal converter 1 may display in the upper right corner of the screen the icons representative of the digital VTR 3, the VTR 7, and the operation of copying a moving image. Furthermore, it will be apparent to those skilled in the art that, when copying a still image from the Memory Stick 10 to a Memory Stick loaded on the digital VTR 3 for example, the signal converter 1 may display the icon 101, the icon 106, and an icon representative of the Memory Stick loaded on the digital VTR 3.

[0113] It should be understood that the controller 67 of the signal converter 1 may cause the superimpose block 68 to display a predetermined icon or character corresponding to the use state of the Memory Stick 10 instead of turning on/off the LED 14.

[0114] Also, it should be understood that storage media for providing the computer programs for executing the above-mentioned processing may be communication media such as networks and communication satellites in addition to recording media such as a magnetic disc, CD-ROM, and solid memory.

[0115] While the preferred embodiments of the present invention have been described using specific terms, such description is for illustrative purposes only,

and it is to be understood that changes and variations may be made without departing from the spirit or scope of the appended claims.

Claims

1. An image processing apparatus on which a first storage medium (9) and a second storage medium (10) are loaded, comprising:

a first converting means (62) for converting inputted first digital video data into a first still image, a second still image stored in said first storage medium into second digital video data, and a third still image stored in said second storage medium into third digital video data;

a second converting means (63) for converting an inputted first analog video signal into a fourth still image, a second still image stored in said first storage medium into a second analog video signal, and a third still image stored in said second storage medium into a third analog video signal;

a first read/write means for writing one of said first still image converted by said first converting means, said fourth still image converted by said second converting means, and a third still image stored in said second storage medium (10) loaded on said image processing apparatus (1) to said first storage medium (9) loaded on said image processing apparatus (1) and reading said second still image stored in said first storage medium (9) loaded on said image processing apparatus (1);

a second read/write means for writing one of said first still image converted by said first converting means, said fourth still image converted by said second converting means, and said second still image stored in said first storage medium (9) loaded on said image processing apparatus (1) to said second storage medium (10) loaded on said image processing apparatus (1) and reading said third still image stored in said second storage medium (10) loaded on said image processing apparatus (1); and

a control means (67) for controlling the conversion by said first converting means, the conversion by said second converting means, the writing and reading by said first read/write means, and the writing and reading by said second read/write means.

2. An image processing apparatus according to claim 1, further comprising:

a remote commander (2); and

a command receiving means through which said image processing apparatus (1) is oper-

ated by said remote commander.

3. An image processing apparatus according to claim 1 or 2, wherein said first storage medium is a PC card (9) and said second storage medium is a Memory Stick (10). 5
4. An image processing apparatus according to any one of claims 1 to 3, wherein said first digital video data, said second digital video data, and said third digital video data are based on DV VCR format. 10
5. An image processing apparatus according to any one of claims 1 to 4, wherein said first still image, said second still image, said third still image, and said fourth still image are based on JPEG format. 15
6. An image processing method for an image processing apparatus on which a first storage medium (9) and a second storage medium (10) are loaded, comprising: 20

a first converting step for converting inputted first digital video data into a first still image, a second still image stored in said first storage medium into second digital video data, and a third still image stored in said second storage medium into third digital video data; 25

a second converting step for converting an inputted first analog video signal into a fourth still image, a second still image stored in said first storage medium into a second analog video signal, and a third still image stored in said second storage medium into a third analog video signal; 30 35

a first read/write step for writing one of said first still image converted by said first converting step, said fourth still image converted by said second converting step, and a third still image stored in said second storage medium loaded on said image processing apparatus to said first storage medium loaded on said image processing apparatus and reading said second still image stored in said first storage medium loaded on said image processing apparatus; 40 45

a second read/write step for writing one of said first still image converted by said first converting step, said fourth still image converted by said second converting step, and said second still image stored in said first storage medium loaded on said image processing apparatus to said second storage medium loaded on said image processing apparatus and reading said third still image stored in said second storage medium loaded on said image processing apparatus; and 50 55

a control step for controlling the conversion by said first converting step, the conversion by

said second converting step, the writing and reading by said first read/write step, and the writing and reading by said second read/write step.

7. An information providing medium for providing a computer-readable program for having an image processing apparatus (1) on which a first storage medium (9) and a second storage medium (10) are loaded execute processing including:

a first converting step for converting inputted first digital video data into a first still image, a second still image stored in said first storage medium into second digital video data, and a third still image stored in said second storage medium into third digital video data;

a second converting step for converting an inputted first analog video signal into a fourth still image, a second still image stored in said first storage medium into a second analog video signal, and a third still image stored in said second storage medium into a third analog video signal;

a first read/write step for writing one of said first still image converted by said first converting step, said fourth still image converted by said second converting step, and a third still image stored in said second storage medium loaded on said image processing apparatus to said first storage medium loaded on said image processing apparatus and reading said second still image stored in said first storage medium loaded on said image processing apparatus; a second read/write step for writing one of said first still image converted by said first converting step, said fourth still image converted by said second converting step, and said second still image stored in said first storage medium loaded on said image processing apparatus to said second storage medium loaded on said image processing apparatus and reading said third still image stored in said second storage medium loaded on said image processing apparatus; and

a control step for controlling the conversion by said first converting step, the conversion by said second converting step, the writing and reading by said first read/write step, and the writing and reading by said second read/write step.

FIG. 1

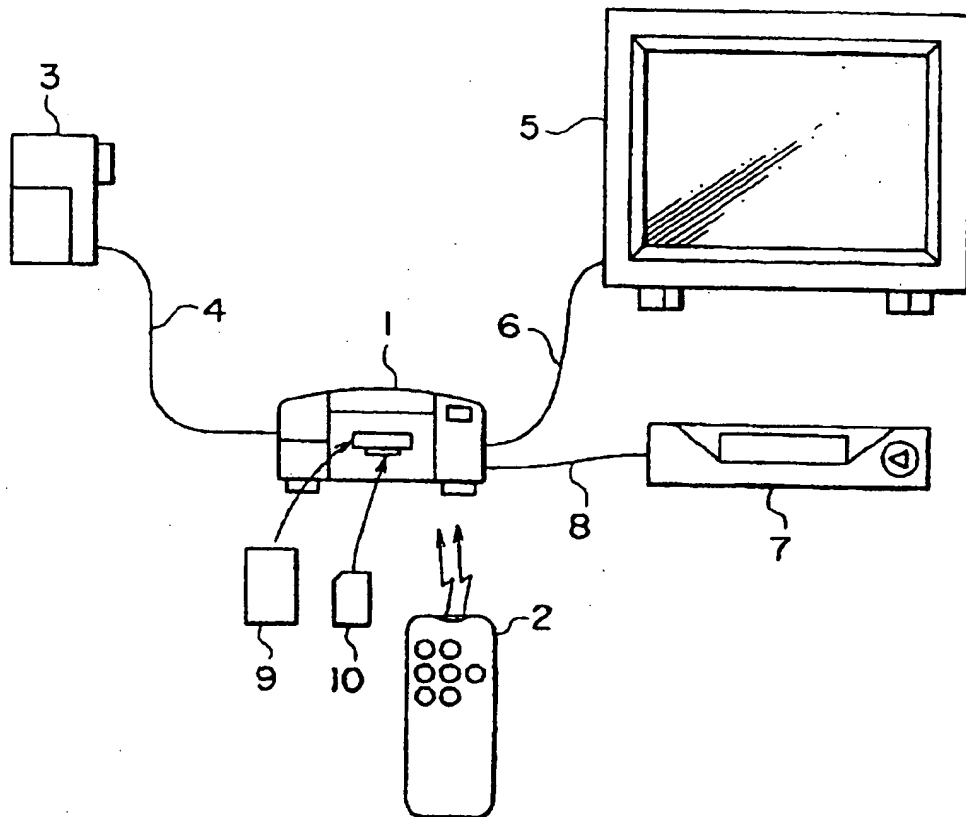


FIG. 2

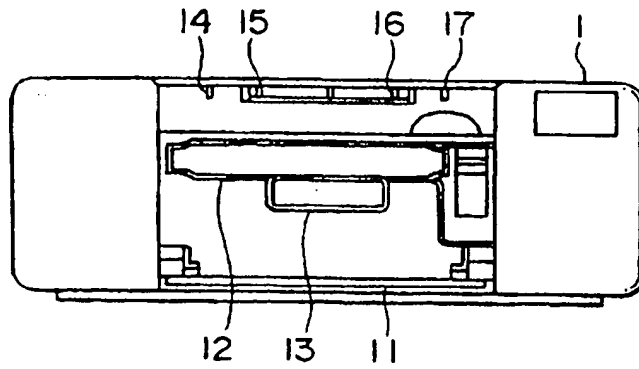


FIG. 3

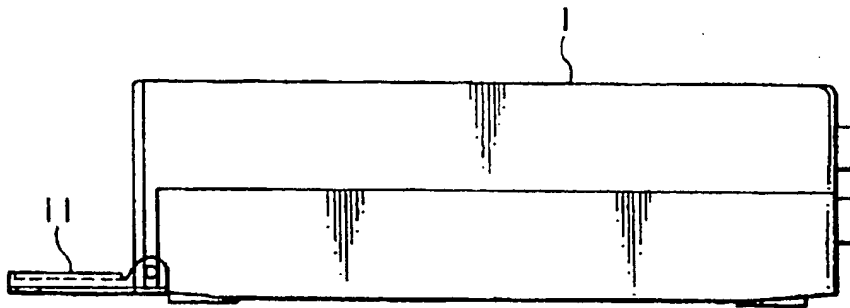


FIG. 4

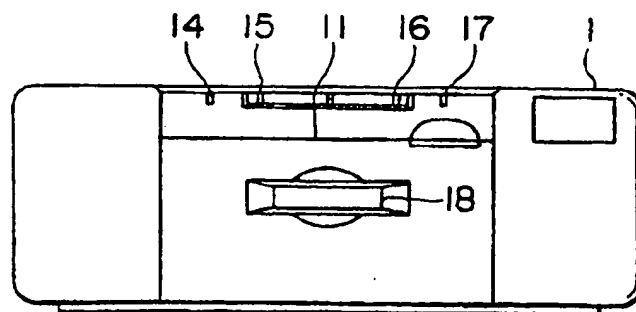


FIG. 5

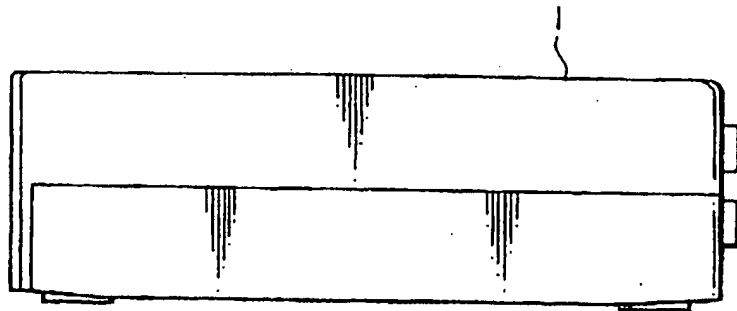


FIG. 6

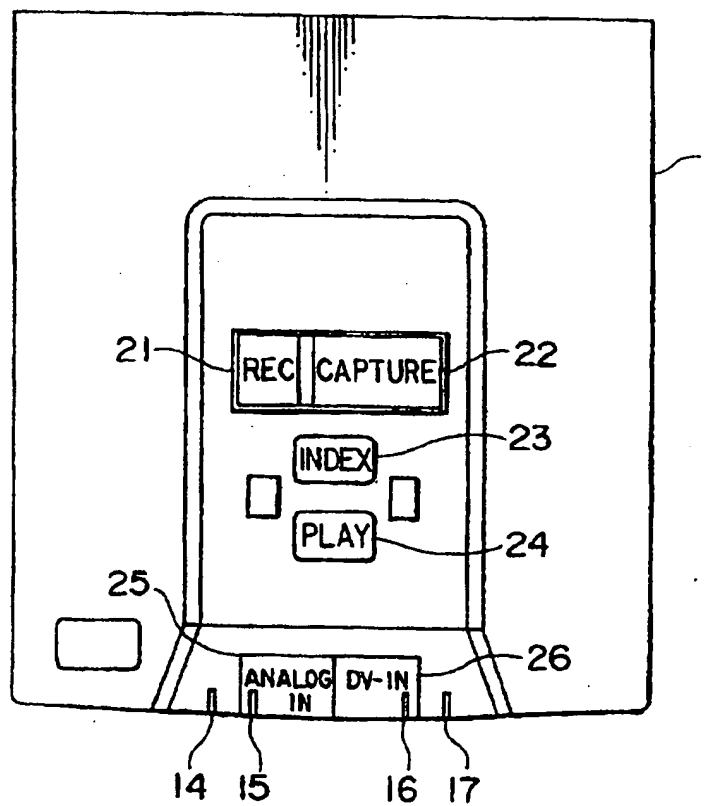


FIG. 7

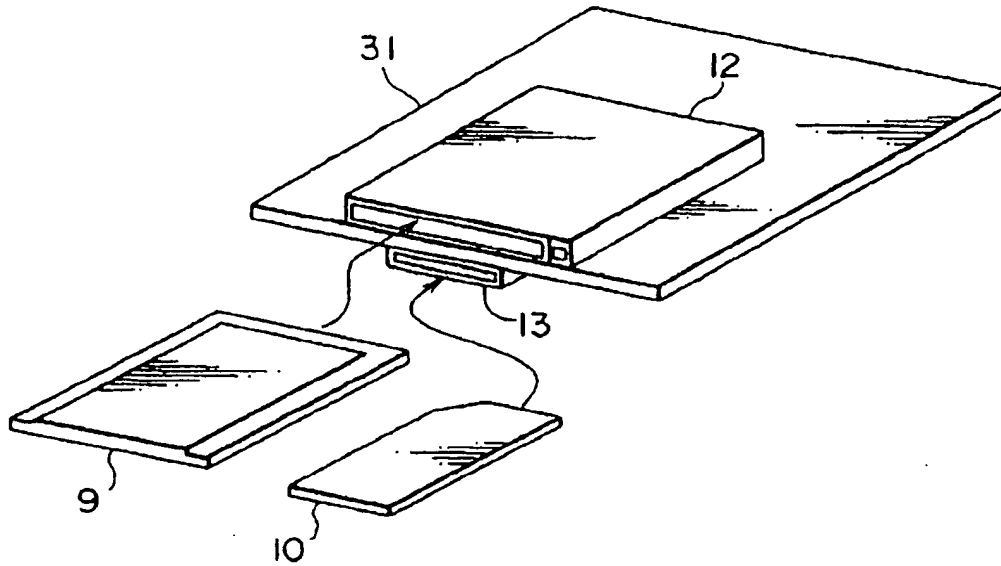


FIG. 8

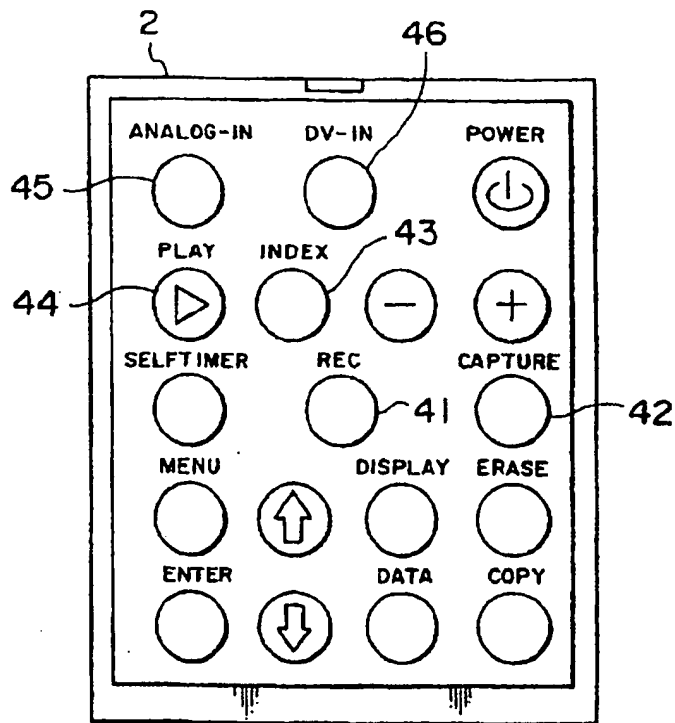


FIG. 9

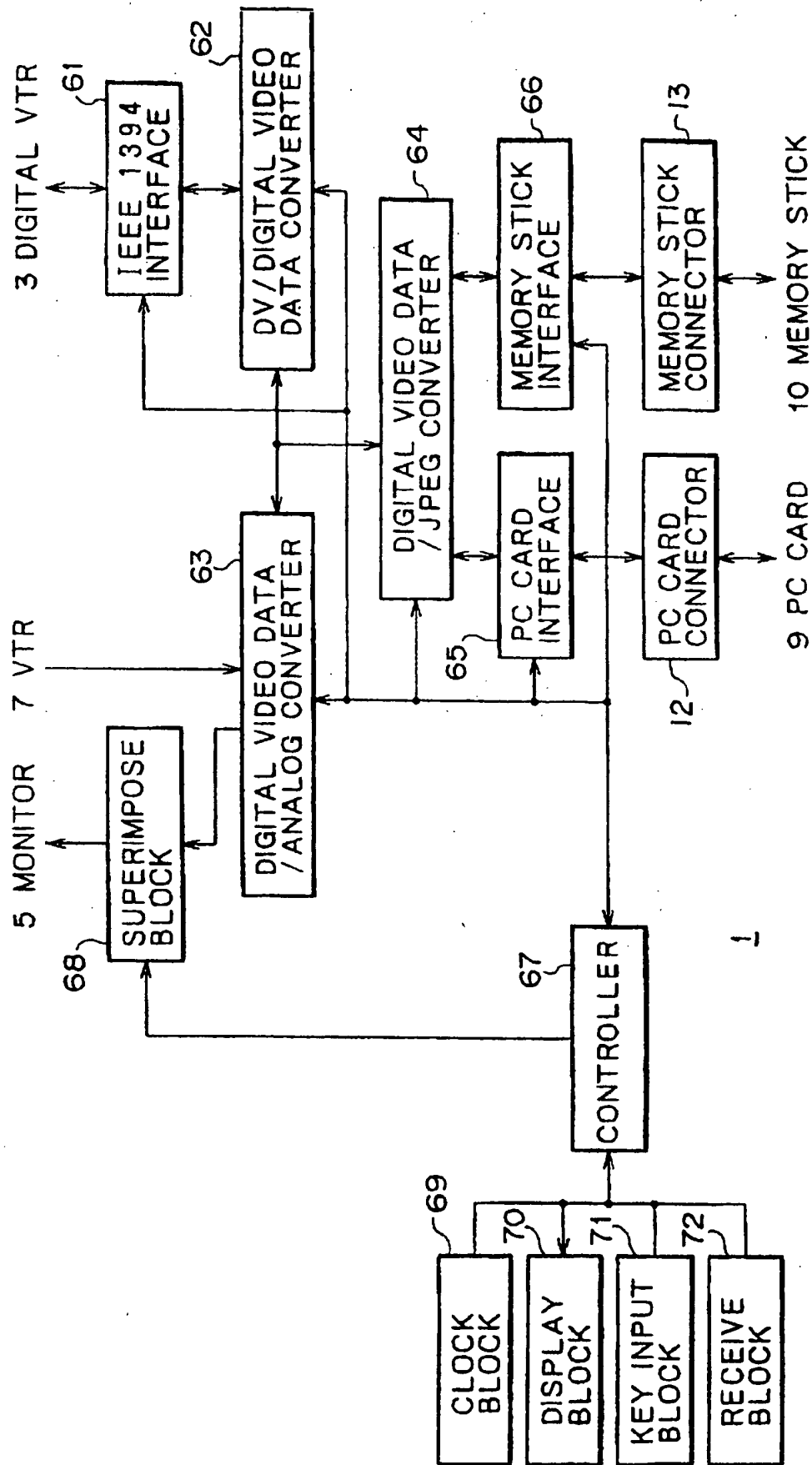


FIG. 10

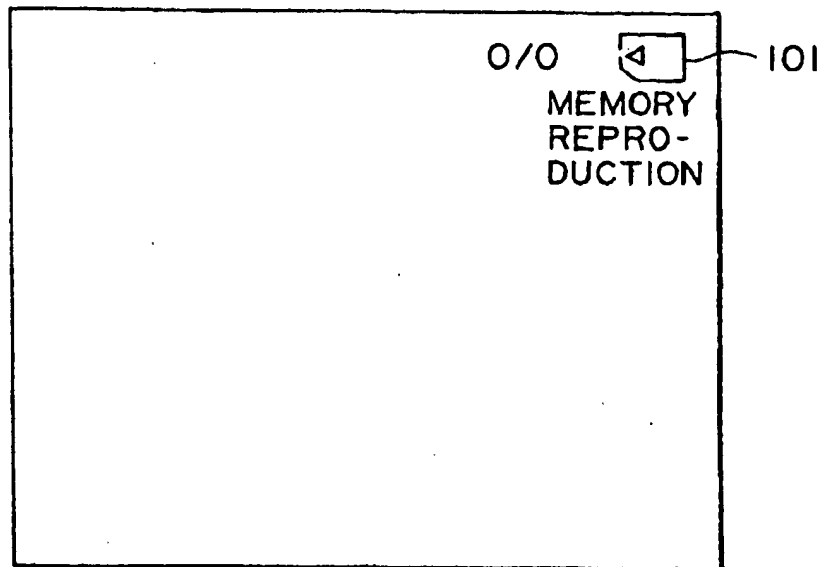


FIG. 11

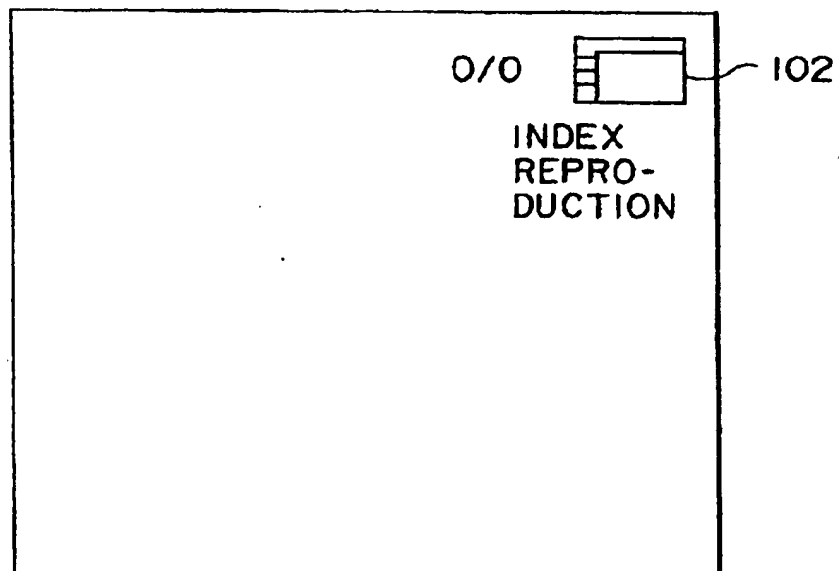


FIG. 12

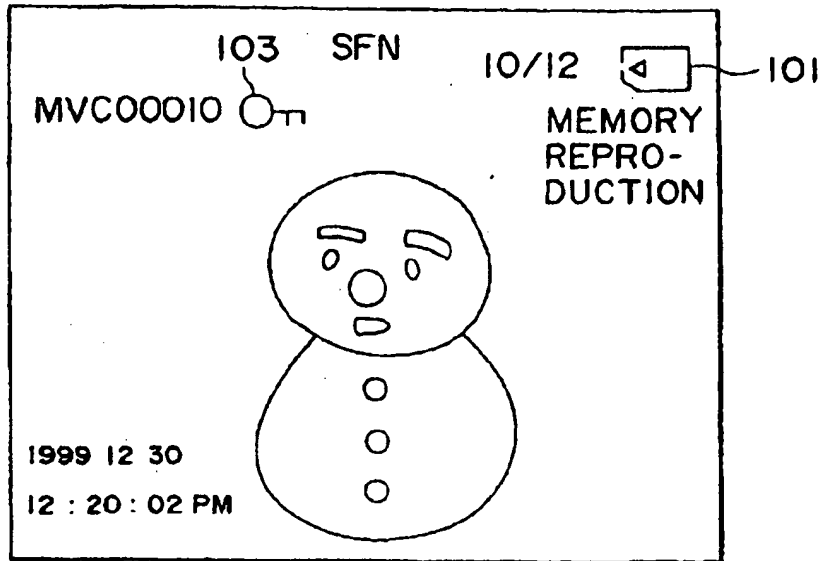


FIG. 13

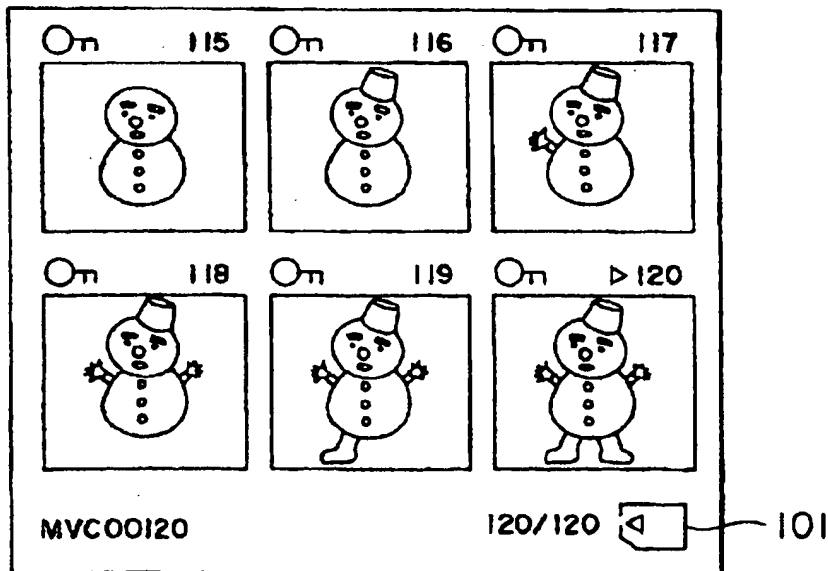


FIG. 14

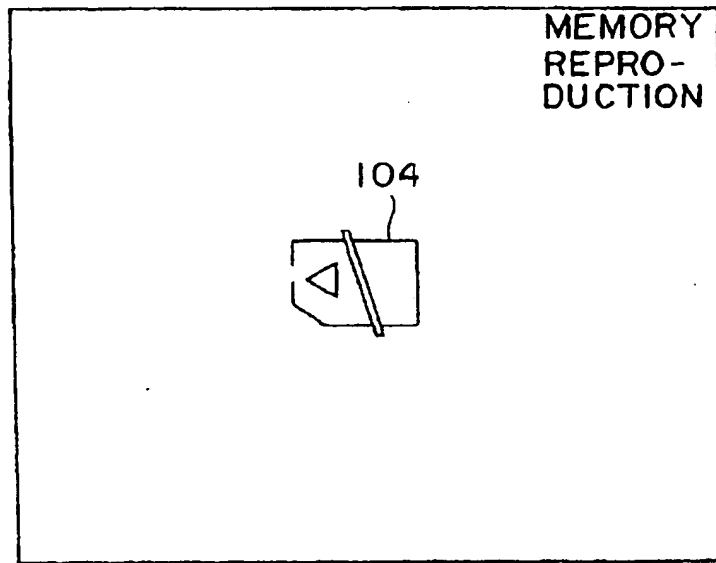


FIG. 15

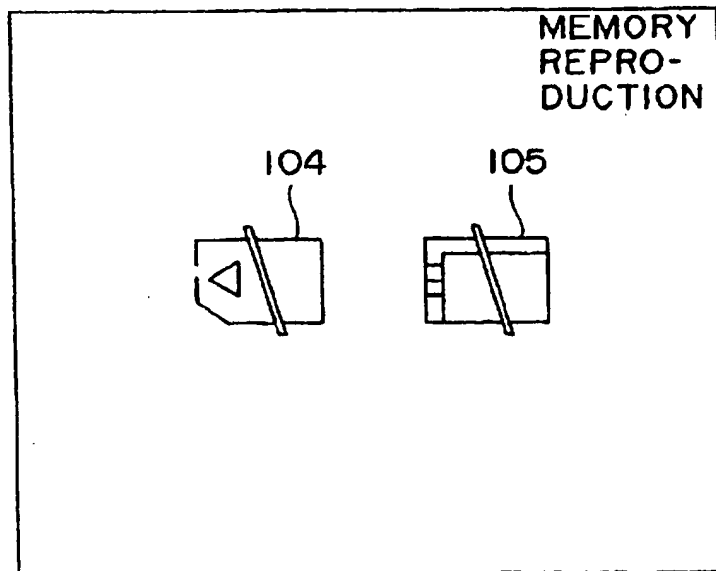


FIG.16

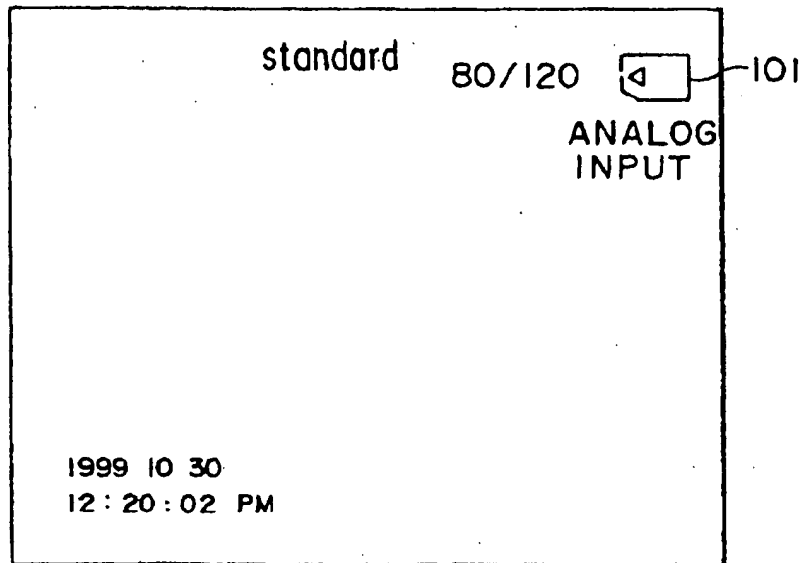


FIG.17

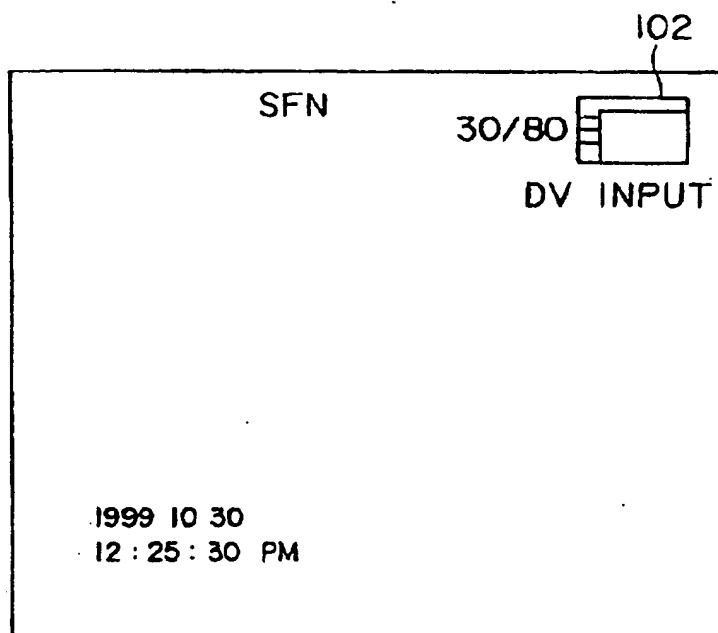


FIG. 18

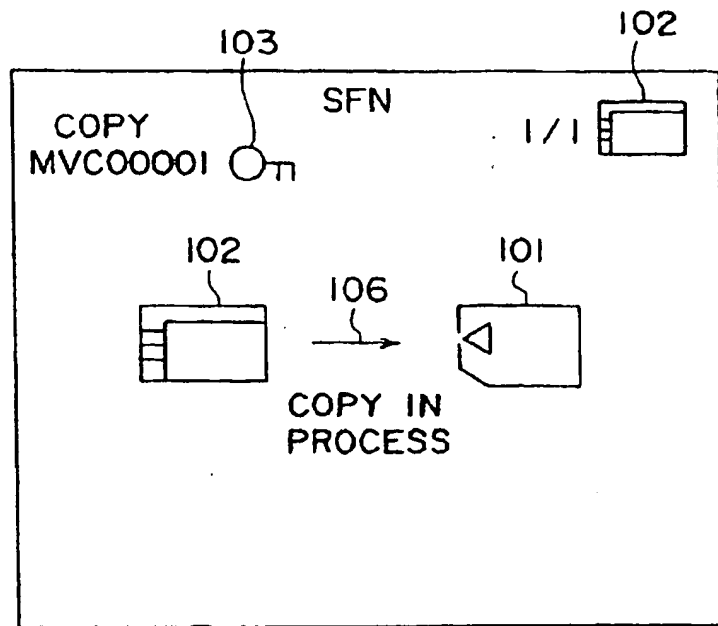
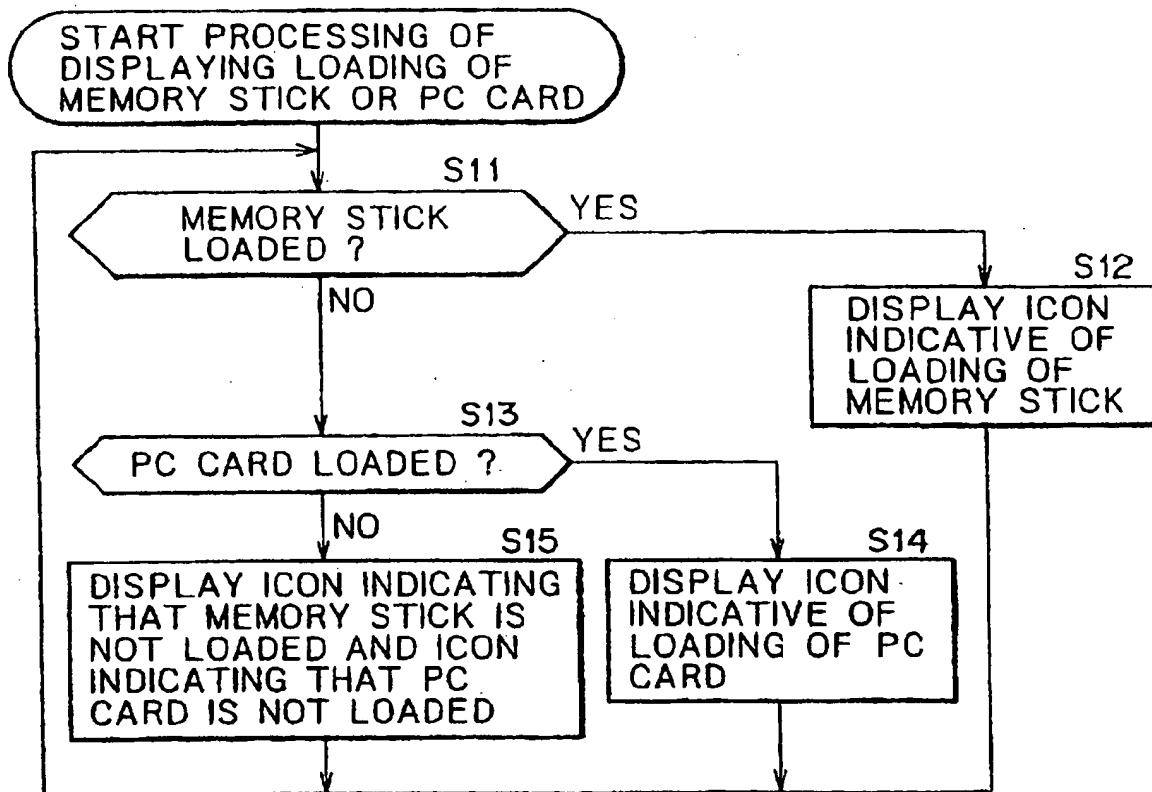


FIG. 19



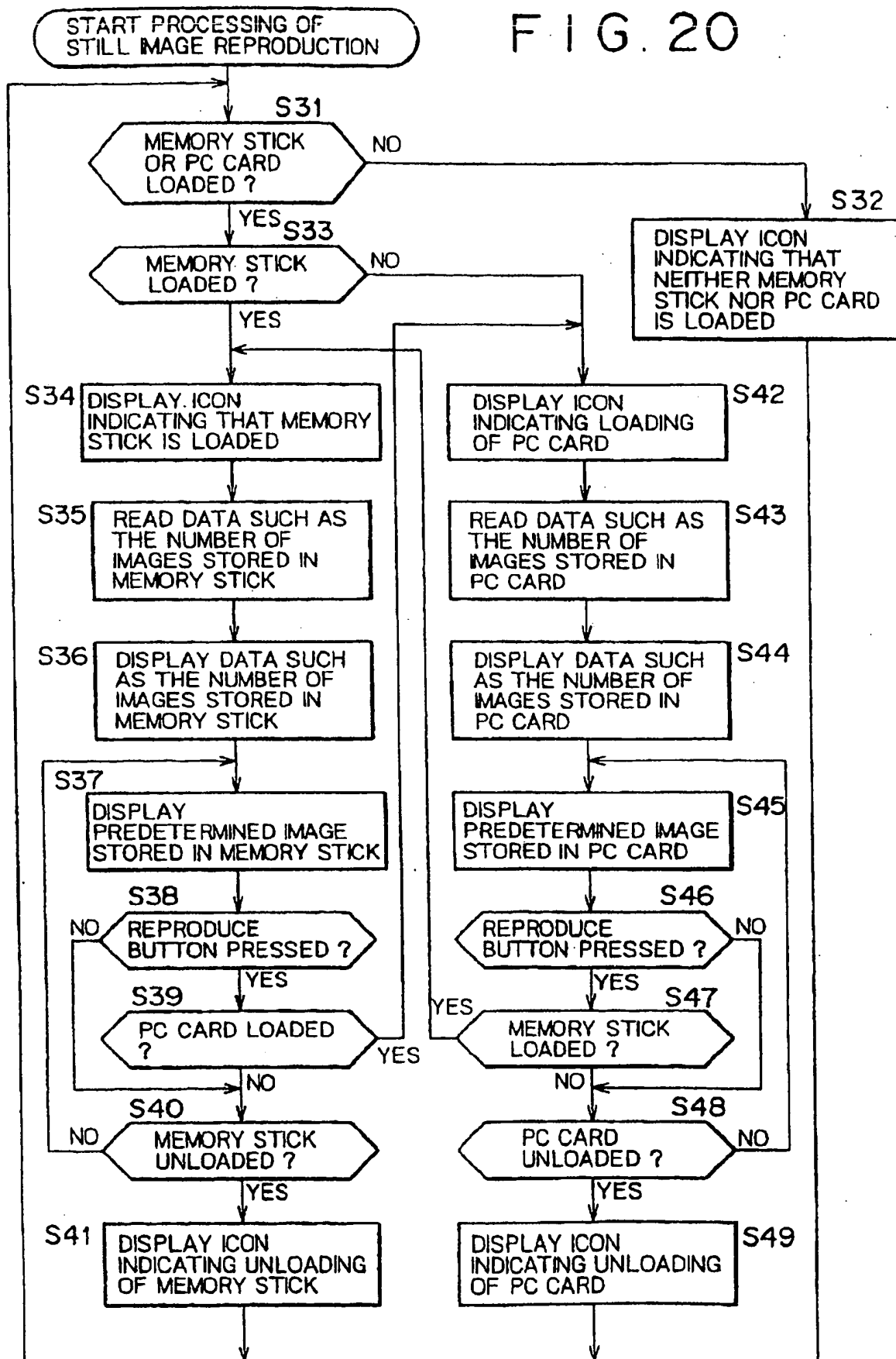


FIG. 21

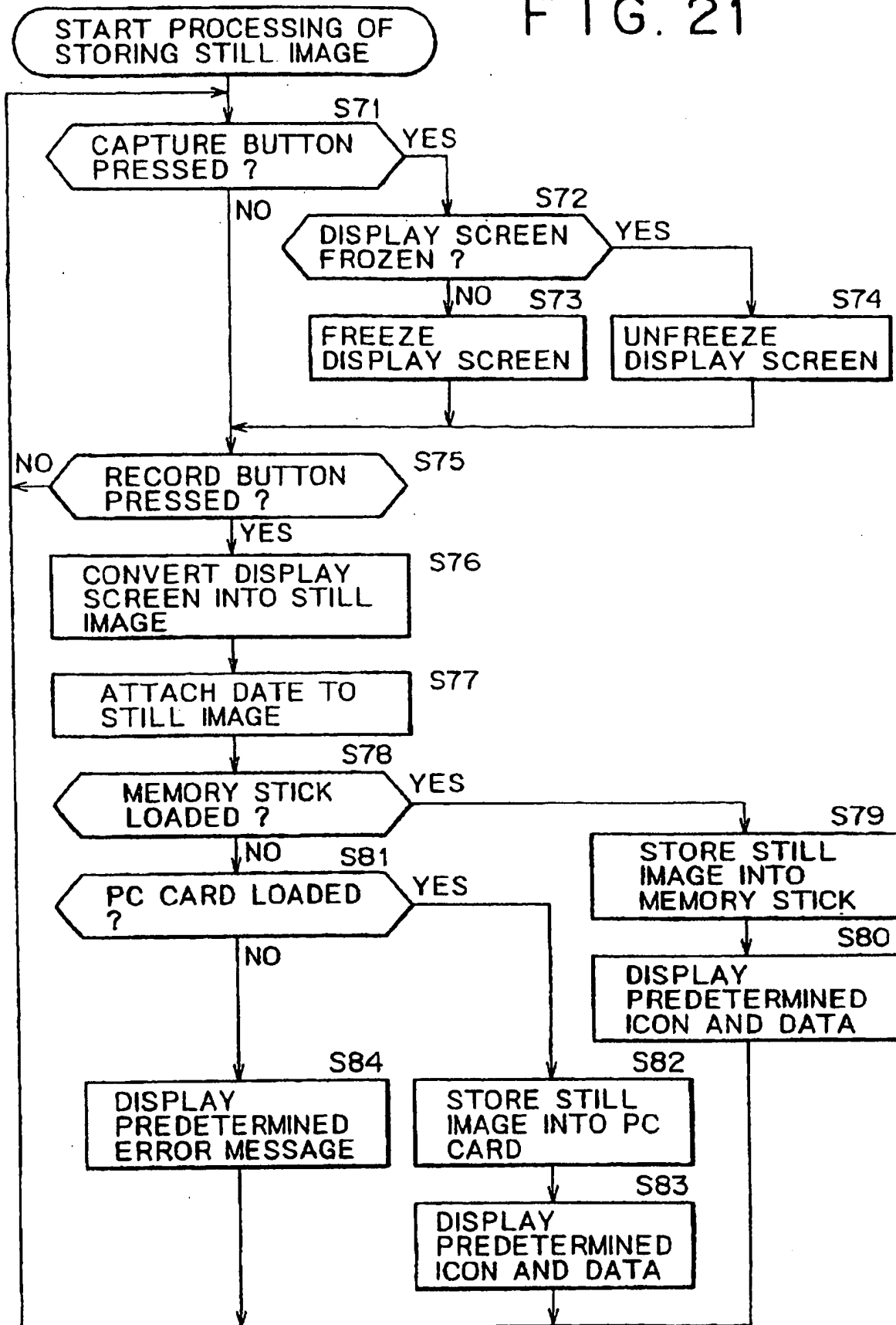


FIG. 22

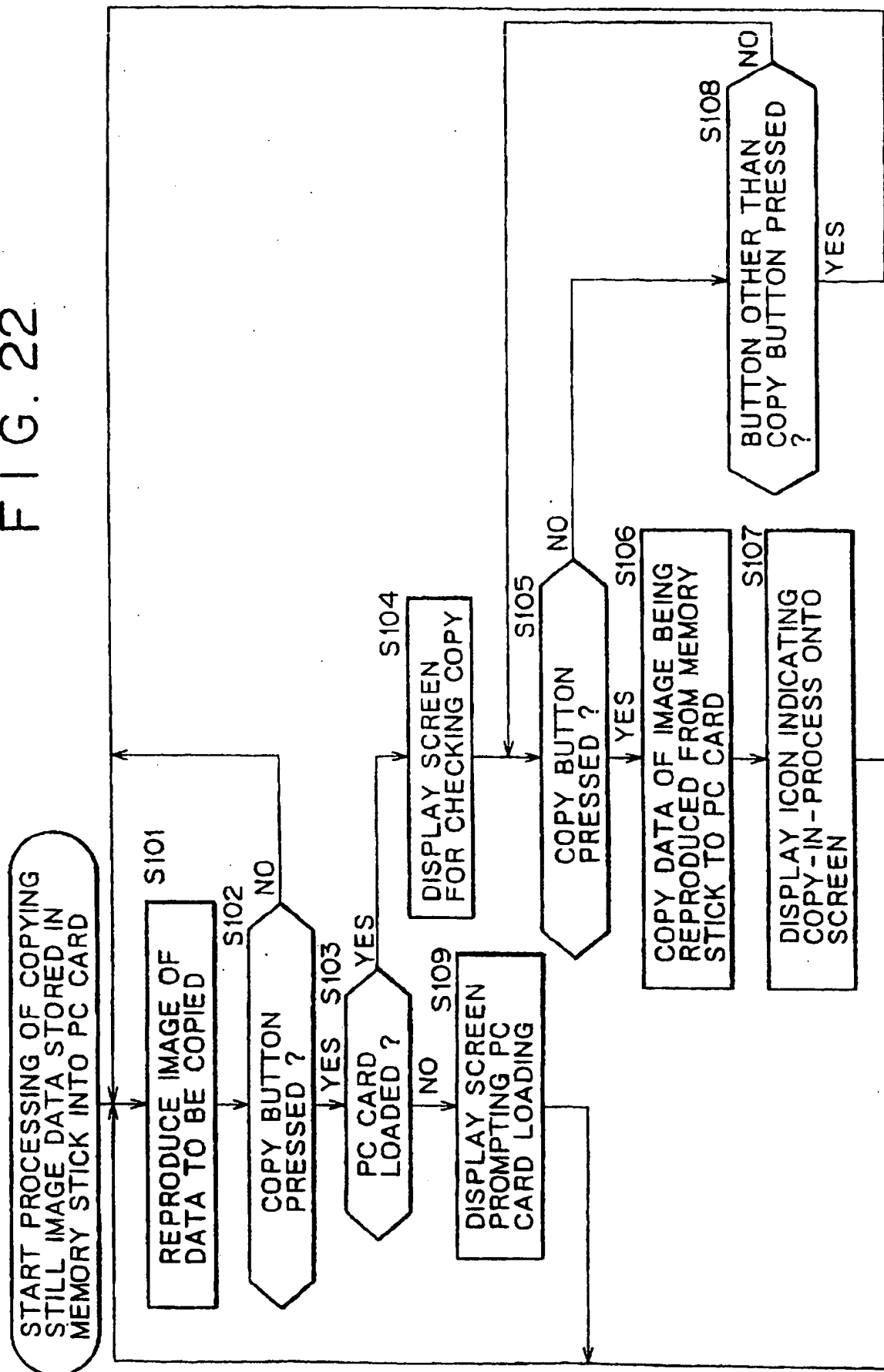
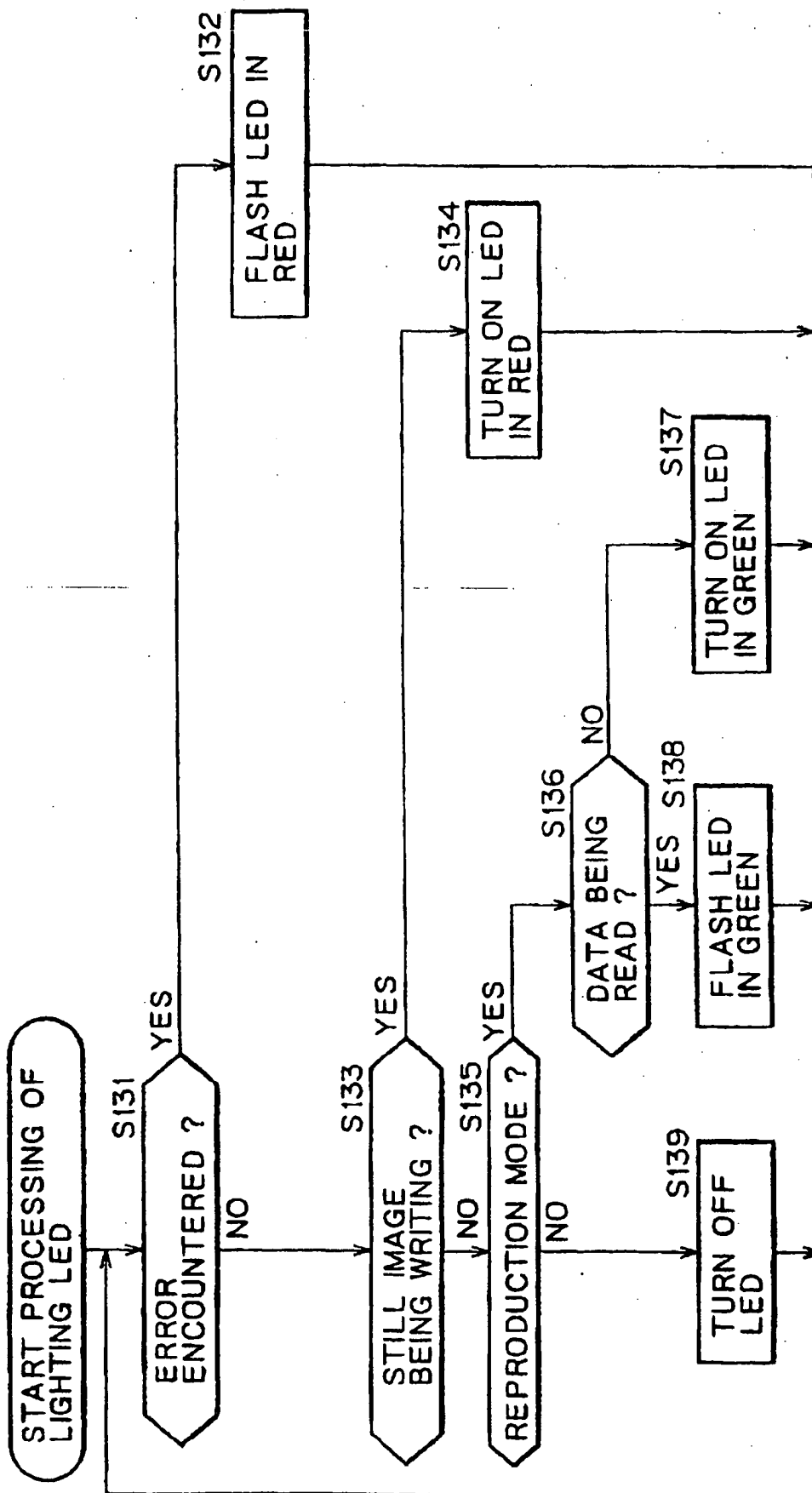


FIG. 23





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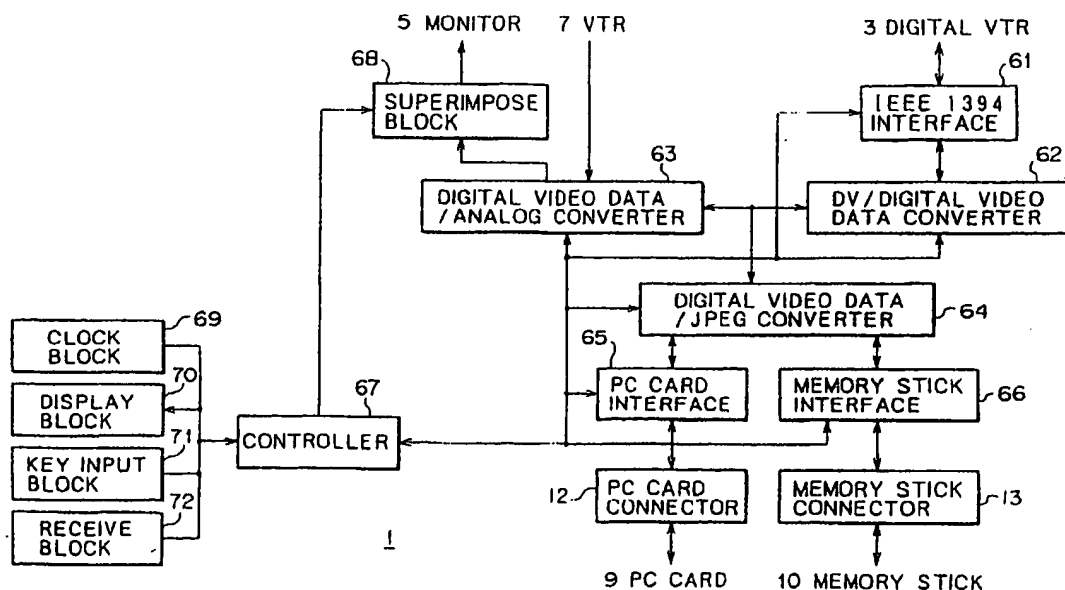
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(54) Image processing apparatus, image processing method and information providing medium

(57) An image processing apparatus and method for facilitating a still image copying operation between a plurality of storage media. Under the control of the controller 67, a DV/digital video data converter 62, a digital video data/analog converter 63, and a digital video data/JPEG converter 64 convert input digital video data into a still image, convert the still image into digital video data

or an analog video signal, and convert an input analog video signal into a still image. Under the control of the controller 67, a PC card interface 65 stores the still image into a PC card 9 and reads the still image therefrom. Under the control of the controller 67, a Memory Stick interface 66 stores a still image into a Memory Stick 10 and reads the still image therefrom.

FIG. 9





European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
A	US 5 488 433 A (WASHINO ET AL.) 30 January 1996 (1996-01-30) * column 3, line 19 - column 5, line 48; figures 1-3 *	1,3,5-7	H04N5/765
A	WO 98 18255 A (PHILIPS ELECTRONICS N. V. ET AL.) 30 April 1998 (1998-04-30) * page 4, line 25 - page 8, line 20; figures 1,2 *	1,3,6,7	
A	US 5 821 996 A (KAWAMURA ET AL.) 13 October 1998 (1998-10-13) * column 1, line 39 - line 59; figure 1 *	1,3,6,7	
A	US 5 862 218 A (STEINBERG) 19 January 1999 (1999-01-19) * column 3, line 45 - line 59; figure 1 *	1,3,6,7	
A	EP 0 859 269 A (SANYO ELECTRIC CO., LTD) 19 August 1998 (1998-08-19) * column 4, line 50 - column 5, line 36; figure 1 *	1,3,5-7	
A	EP 0 823 816 A (HITACHI, LTD.) 11 February 1998 (1998-02-11) * column 2, line 53 - column 3, line 36; figure 1 *	1,3,6,7	
A	US 5 764 800 A (YAMAGATA) 9 June 1998 (1998-06-09) * the whole document *	1,3,6,7	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 23 August 2002	Examiner Verleye, J
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03/02 (P04/C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 00 10 3063

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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23-08-2002

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5488433	A	30-01-1996	US 5537157 A	16-07-1996
			US 5450140 A	12-09-1995
			WO 9727704 A1	31-07-1997
			CA 2213973 A1	06-09-1996
			DE 69621782 D1	18-07-2002
			EP 0812509 A1	17-12-1997
			JP 11507184 T	22-06-1999
			US RE37342 E1	28-08-2001
			WO 9627263 A1	06-09-1996
			EP 0997039 A1	03-05-2000
			US 5625410 A	29-04-1997
			CA 2161112 A1	27-10-1994
			JP 8509337 T	01-10-1996
			WO 9424813 A1	27-10-1994
			JP 2001501781 T	06-02-2001
WO 9818255	A	30-04-1998	EP 0868814 A2	07-10-1998
			WO 9818255 A2	30-04-1998
			JP 2000503501 T	21-03-2000
US 5821996	A	13-10-1998	JP 2118990 A	07-05-1990
			JP 2219376 A	31-08-1990
			JP 2712489 B2	10-02-1998
			JP 2257341 A	18-10-1990
			US 6118929 A	12-09-2000
			DE 68920273 D1	09-02-1995
			DE 68920273 T2	11-05-1995
US 5862218	A	19-01-1999	EP 0366474 A1	02-05-1990
			EP 0966839 A1	29-12-1999
			JP 2001517383 T	02-10-2001
EP 859269	A	19-08-1998	WO 9738531 A1	16-10-1997
			JP 10229540 A	25-08-1998
			EP 0859269 A2	19-08-1998
EP 823816	A	11-02-1998	US 6351600 B1	26-02-2002
			JP 10051722 A	20-02-1998
			EP 0823816 A2	11-02-1998
US 5764800	A	09-06-1998	KR 261705 B1	15-07-2000
			JP 8331498 A	13-12-1996

EPO FORM 10459

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